

APPLICATION DATA SHEET

APPLICATION INFORMATION

Filing Date:: 07/03/03
Application Type:: Regular
Subject Matter:: Utility
Title:: Softpal Implementation and Mapping
Technology for FPGAs with Dedicated
Resources
Attorney Docket Number:: X-949 US
Request for Early Pub?:: No
Request for Non-Pub?:: Yes
Total Drawing Sheets:: 8
Small Entity?:: No
Petition included?:: No

APPLICANT INFORMATION

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: India
Status:: Full Capacity
Given Name:: Satish
Middle Name:: R.
Family Name:: Ganesan
Street:: 215 Ortega Avenue
City:: Mountain View
State or Province:: CA
Postal or Zip Code:: 94040

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: India
Status:: Full Capacity
Given Name:: Sundararajao
Family Name:: Mohan
Street:: 510 Kinross Court
City:: Sunnyvale

State or Province:: CA
Postal or Zip Code:: 94087

Applicant Authority Type:: Inventor
Primary Citizenship Ctry:: Germany
Status:: Full Capacity
Given Name:: Ralph
Middle Name:: D.
Family Name:: Wittig
Street:: 2357 Sharon Road
City:: Menlo Park
State or Province:: CA
Postal or Zip Code:: 94025

CORRESPONDENCE INFORMATION

Correspondence Customer Number:: 24309

REPRESENTATIVE INFORMATION

R representative Customer Number::	24309	
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ASSIGNEE INFORMATION

Assignee Name:: Xilinx, Inc.
Street:: 2100 Logic Drive
City:: San Jose
State or Province:: California
Postal or Zip Code:: 95124